

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete If Known		
			Application Number	10/035,633	
			Filing Date	12/28/2001	
			First Named Inventor	Eldridge et al.	
			Group Art Unit	2841	
Examiner Name	Unknown CARL Arbes				
Sheet	1	of	2	Attorney Docket No.	P3C1-US

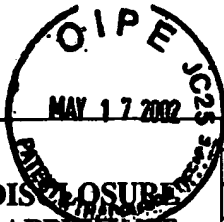
U.S. PATENT DOCUMENTS

Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant	Publication Date	Relevant Portions
		Number	Kind Code ²			
QA	1	3,389,457		Goldman et al.	6/25/1968	
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Examiner Signature	C. J. Arbes	Date Considered	4/25/05
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		Office ³	Number ⁴	Kind Code ⁵				
<i>my</i>	35	FR	2680284		Christian	2/12/1993		
<i>cja</i>	36	JP	4240570		Naoaki	8/27/92		
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<i>cja</i>	44	JP	61244057		Kato	10/30/1986		

OTHER PRIOR ART - NONPATENT LITERATURE DOCUMENTS			
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
<i>cja</i>	45	Duane W. ENDICOTT and John R. KNAPP, Jr. "Electrodeposition of Nickel-Cobalt Alloy: Operating Variables And Physical Properties Of The Deposits" (January 1966), pp. 43-60, (Plating).	
<i>cja</i>	46	Shih, D.-Y et al., "A Novel Elastomeric Connector for Packaging Interconnections, Testing and Burn-in Applications", IEEE, Proceedings of the Electronic Components and Technology Conference, Las Vegas, NV, May 21, 1995.	
<i>qm</i>	47	Weast, Handbook of Chemistry and Physics, CRC Press, Cleveland, OH, pp.D171-172, (1975-1976).	

Examiner Signature	<i>C. J. Arbes</i>	Date Considered	4/25/05
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Group Art Unit	2841 3729
Examiner Name	Unknown Arbes
Attorney Docket No.	P3C1-US

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CJA	50	JP	61287254		Komabayashi et al	12/1986
CJA	51	JP	61291991		Inetani et al	12/1986
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CJA	55	JP	62120057		Tanigawa et al	6/1987
CJA	56	JP	02034949		Bessho et al	2/1990
CJA	57	JP	02183538		Asahina	7/1990
CJA	58	JP	3142847		Ishida	6/1991
CJA	59	JP	05259334		Sasaki	10/1993

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Cja	60	"Method of Testing Chips and Joining Chips to Substrates" Research Disclosure, No. 322, p.130, February 1991.	
ga	61	"Bitmetal VLSI Chip Interconnections" IBM Technical Disclosure Bulletin, Vol.29, No. 11, pp. 5021-5022, April 1987.	
Cja	62	"Method for Large Board Engineering Changes and Repair", IBM Technical Disclosure Bulletin, Vol.37, No. 02B, pp. 583-585, February 1994.	
Cja	63	"Engineering Change Interposer" IBM Technical Disclosure Bulletin, Vol.37, No. 9, p.157, September 1994.	
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Cja	65	"Bare-Chip-Attachment Method Spread I/O, Permits Pre-testing", 2328 Electronic Design, Vol.39, No. 10, May 23, 1991.	
Cja	66	IBM Technical Disclosure Bulletin, Vol. 29, No. 8, p.3754, January 1987.	

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